Remarks

Claims 1, 2, 7, 8, 10, 12, 13, 15, 17, 19, 21, 26 and 30 are amended. Claims 3, 9, 14, 16, 23 and 27 are cancelled. Claims 31 through 40 are added. Claims 1, 2, 4 – 8, 10 – 13, 15, 17 – 22, 24, 25, 26 and 28 - 40 are pending. Reexamination and reconsideration of this application, as amended, are respectfully requested.

The Examiner has objected to the Drawings as not showing certain features directed to an "operating system," "utility program" and a "procedure to enumerate the peripheral device" under 37 CFR 1.83(a). The applicant is submitting herewith corrections Figure 2 to address objections with respect to the phrases "operating system" and "utility program" in addition to corrections to address other informalities. In addition to being self supporting in the claims, these limitations are further supported in the specification at page 12, line 14, to page 13, line 15. The applicant is also submitting an additional Figure 3 to illustrate the limitations "procedure to enumerate the peripheral device." In addition to being self supporting in the claims, these limitations are further supported in the specification at page 9, line 13 to page 10, line 5. Accordingly, these amendments to the Figures are provided to merely illustrate subject matter which has already been described in the specification and the applicant submits that no new subject matter is being added as a result of the amendment to the Figures.

The Examiner objected to claims 1 and 3 for informalities relating to the phrase "host processing in claim 1 and "a storage medium" in claim 3. The applicant has corrected the informality in claim 1 and cancelled claim 3.

The Examiner has rejected claims 1, 7, 13, 19 and 25 as being anticipated by U.S. Patent No. 5,694,600 to Khenson (the "'600 patent") under 35 U.S.C. § 102 (b) or

U.S. Patent No 6,009,480 to Pleso (the "480 patent") under 35 U.S.C. § 102 (e). This rejection is respectfully traversed.

The specification of the present application relates to the use of an agent hosted on a host processing system. The agent may be created from machine-readable instructions loaded from a peripheral device coupled to the host processing system by a data bus. As part of a procedure to enumerate the peripheral device on the data bus, the machine-readable instructions are transmitted from an option ROM of the peripheral device to the host processing system.

Once loaded, the agent may load instructions to a location of RAM in the host processing system which, when executed, may cause the host processing system to retrieve a bootable image (e.g., including an operating system) from the peripheral device. Upon detection of an event, the agent may modify an interrupt vector 13 *h* in the host processing system from specifying an address in a master boot record of a hard drive to the location in RAM where the agent has previously loaded instructions, and initiate a reset event at the host processing system to commence execution at the location in RAM specified by the address in the modified interrupt vector 13 *h*. The host processing system may then retrieve the bootable image from the peripheral device upon executing the instructions from RAM. [Specification, page 11, line 3 – page 12, line13]

The '600 patent appears to show a block diagram of a computer system comprising a storage device subsystem 250 that is removable from a personal computer 200 that may be used as hard drive device. The '480 patent appears to show a peripheral device (from among peripheral devices P1 through P4) that includes a

memory for storing a device driver that may be downloaded to a computer system 10 when the peripheral device is coupled to the computer system 10.

Claim 1 of the present application distinguishes over the devices shown in the '600 and '480 patents by reciting, among other things:

> logic to modify an interrupt vector address to specify execution of machine-readable instructions at a location in the random access memory instead of at a location in the non-volatile memory;

Neither the '600 patent nor the '480 patent appears to disclose, suggest or make obvious a modification of "an interrupt vector address to specify execution of machine readable instructions at a location in the random access memory instead of at a location in the non-volatile memory." Accordingly, the applicant respectfully submits that claim 1, and claims 2, 4, 5 and 6 depending therefrom, distinguish over the '600 and '480 patents. While differing in scope at least in part, the remaining claims recite similar limitations to those in claim 1 which are quoted above. Accordingly, the applicant respectfully submits that these claims similarly distinguish over the '600 and '480 patents.

The Examiner also rejected cancelled claim 3 as being obvious in view of the '480 patent and U.S. Patent No. 5,832,283 to Chou et al. under 35 U.S.C. § 103 (a). The '283 patent appears to show a system for initiating a system reset event in response to cold boot procedure to put a computer system in a known operating state. ['283 patent, col. 10, ll. 22 – 35] However, the '283 patent does not show modification of "an interrupt vector address to specify execution of machine readable instructions at a location in the random access memory instead of at a location in the non-volatile memory."

Accordingly, the applicant submits that the claims distinguish over the '283 patent.

The applicant respectfully submits that the application is now in form for allowance. Reconsideration of this case is respectfully requested. Please charge Deposit Account #02-2666 for the addition of claims 31 – 40 and any fee payment deficiencies associated with this case. If the Examiner finds that this case is in any way not in proper form for allowance, the applicant requests that the Examiner contact the applicants' representative at (310) 252-7621.

Respectfully submitted,

for Chinmay Patel

by *[signature]*

Paul G. Nagy
Assistant Director
Intel Corporation
Reg. No. 37,896

Dated: May 17, 2004

c/o Blakely, Sokoloff, Taylor & Zafman, LLP
12400 Wilshire Blvd., Seventh Floor
Los Angeles, CA 90025-1026
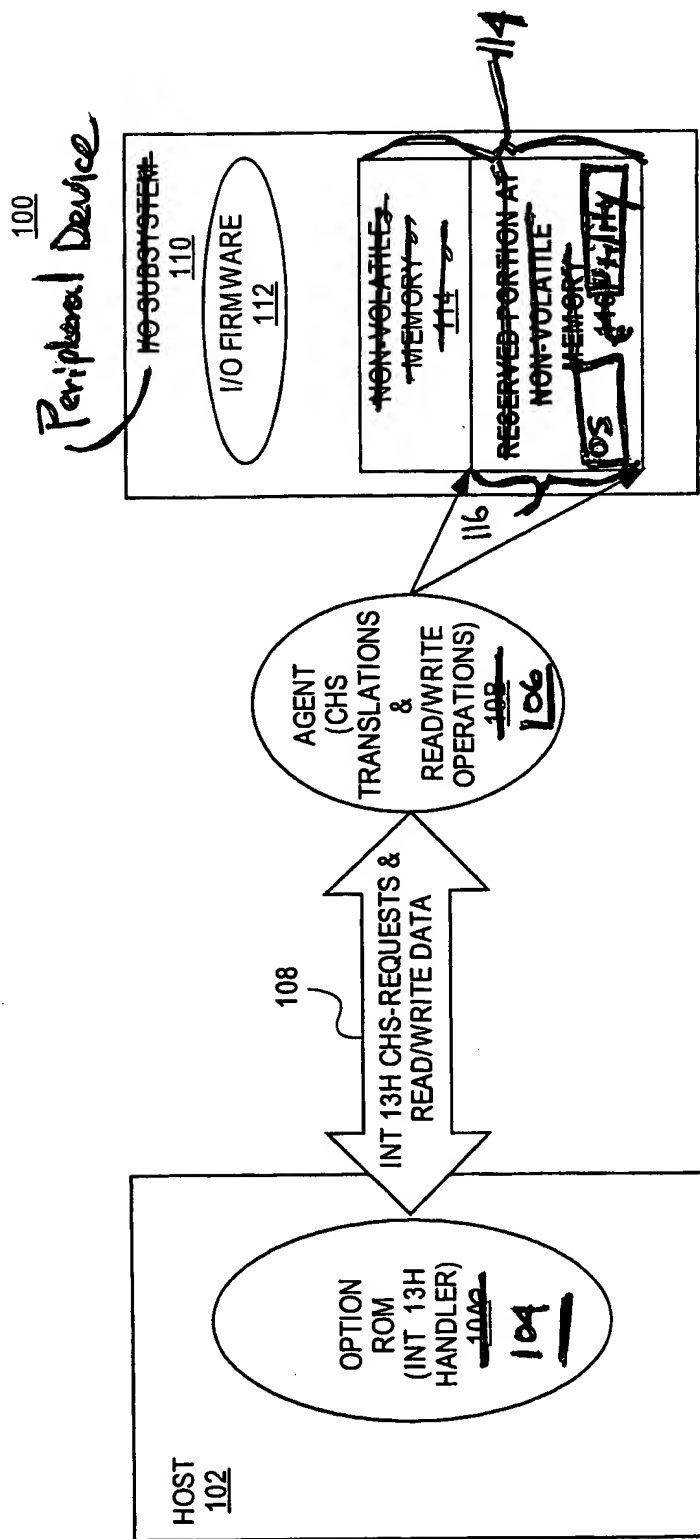(310) 207-3800
(310) 820-5988

Peripheral Device 100

I/O SUBSYSTEM 110

I/O FIRMWARE 112

NON-VOLATILE MEMORY 114

114

RESERVED PORTION AT NON-VOLATILE MEMORY (114) 114

116

116

AGENT (CHS TRANSLATIONS & READ/WRITE OPERATIONS) 106

108

INT 13H CHS-REQUESTS & READ/WRITE DATA

HOST 102

OPTION ROM (INT 13H HANDLER) 104

FIG. 2